

## REMARKS

In response to the above-identified Office Action, the Applicants submit the below remarks and respectfully requests reconsideration thereof in light of these remarks.

The Examiner rejected claims 1-8 and 14-18 under 35 U.S.C. 102 (e) as being anticipated by U.S. Patent 6,031,472 (hereinafter Johnson). The Applicants respectfully traverse this rejection for the reasons set out below.

Applicants contend that the reference does not teach or suggest all limitations of claim 1, or the other independent claims of the present application. The Applicants' arguments shall be presented with respect to claim 1. However, these comments are applicable to the other independent claims of the present application, and the Examiner is respectfully requested to consider these comments and remarks when reviewing the other independent claims for allowability.

Johnson does not teach or suggest the present invention as claimed, specifically sending a consecutive second biphasic pulse at a second time instance after a time interval, wherein the first biphasic pulse and the second biphasic pulse are different types, wherein the time interval between said first time instance and said second time instance represents at least a first set of the data bits, said first set of data bits comprising more than one data bit. Johnson discloses an encoding/decoding method for digital data transmitter. In Johnson the encoding of the symbol that represents two data bits (11) is represented by two signals, one having an amplitude of +3 and the other signal having an amplitude of -3 for the second half of the period T representing time (Column 8, lines 5-10). Thus, the time interval between two consecutive pulses of different type, specifically +3 amplitude signal and -3 amplitude signal, does not represent a first set of data bits, wherein the data bits comprise more than one bit, because the two consecutive signals together represent two bits. Moreover, even though Johnson states that more levels, for example three positive and three negative levels, may be used to encode symbols representing more than two binary bits each, Johnson does not disclose or suggest how

many bits are represented by consecutive levels in this particular situation and whether the two consecutive signals represent more than one data bit (Column 8, lines 42-45). Therefore, Johnson does not teach or disclose the present invention as claimed for the reasons presented above.

The Applicants submit that the rejection under 35 U.S.C. § 102 (e) has been addressed, and withdrawal of this rejection is respectfully requested. The Applicants furthermore submit that all pending claims are in condition for allowance, which is earnestly solicited.

Authorization is hereby given to charge our Deposit Account No. 02-2666 for any charges that may be due. Furthermore, if an extension is required, then Applicants hereby request such an extension.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP



Dated: June 2, 2003

Saina S. Shamilov  
Reg. No. 48,266

12400 Wilshire Boulevard  
Seventh Floor  
Los Angeles, CA 90025-1026  
(408) 720 - 8300